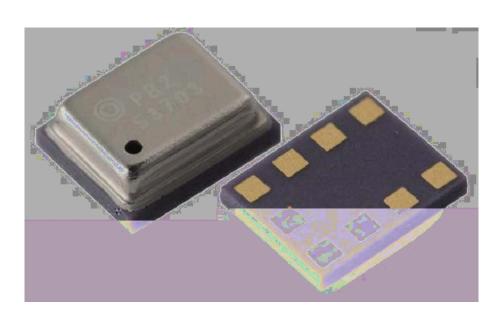
## High accuracy and small size barometric pressure sensor with low current consumption



Structure	Packagi ng	Model	Milminum Packing Unit
LCA 9pin	Tape and Reel	QMP6988	3500

Type of Pressure	Absolute Pressure
Medi um	Air (*1)
Operating Pressure Range	30kPa to 110kPa

Item	Syntbol	Rating	Uni t
Power Supply Voltage	Vddnaux	4.0	V
Input Voltage (other than power)	Vnaax	-0.2~Vopr+0.2	V
Maxi mum Pressure	Pnax	800	kPa
Storage Temperature	Tstr	- 40-85	
Storage Humidity	Hstr	10-95	%RH
ESD(HBM)	Vhbm	± 2000	V
ESD( MM)	Vnnn	± 200	V
ESD(CDM)	Vcdm	± 500	V

Item	Syntbol	Mn	Тур
Conneting Voltage	Vopr	1. 71	1. 8
Operating Voltage	Ио	1. 2	1. 8
Operating Temperature	Topr	- 40	

Item	Syntbol	Condition 1sample/s force-mode	Min	Тур	Max	Unit
Average Current *	Ihp	Utra Hgh Accuracy	_	21. 4	_	μA
O	Iddp	Pressure mode	-	640	800	μA
	I ddt	Tenperature noode	-	410	520	μA
Sleep Mode Current		•				•
Consumption	I sl eep		-	1. 1	23	μA
Measureable	_					·
Pressure Range	Popr		30	-	110	kPa
Absolute	-					
Pressure Accuracy	Pabs 1	30-110kPa, -20 - 65	- 100	-	100	Pa
Relative						
Pressure Accuracy *	Prel 1	Utra Hgh Accuracy	-	± 39	-	Pa
rn <b>s</b> Noise *	Pnoi s	Utra High Accuracy	-	1. 3	-	Pa
Absolute Temperature	<u> </u>					
	r					

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Item	Synbol	Condition	Min	Тур	Max	Uhi t
Digital Input Low Voltage	Wil_d		-	-	Vi o*0. 2	V
Digital Input High Voltage	Vi h_d		Vi o*0.8	-	-	V
Digital Input Hysterisis	Vi dhys		Vi o*0. 1	-	-	V
Digital Output Low Voltage(I2O)	Vol_d1	Io=3mA(SDI)*1)	0	-	Vi o*0. 2	V
Digital Output Low Voltage(SPI)	Vol_d2	Io=1mA(SDI, SDO)*1)	0	-	Vi o*0.2	V
Digital Output High Voltage 1 (SPI)						
(Vi o>=1. 62V)	Voh_d1	Io=1m2A (SDI, SDO) *1)	Vi o*0.8	-	-	V
Digital Output High Voltage 2 (SPI)						
(Vi o>=1. 2V)	Voh_d2	Io=1m2A (SDI, SDO) *1)	Vi o*0.6	-	-	V
Leakage Current at Output OFF	Ioff	SDI, SDO	- 10	-	10	μA
Internal Pullup Resistor	Roullup	CSB	70	120	190	kohm
I2C Load Capacitor	Cb	SDI, SCK	-	-	400	рF
Load Capacitance of						
Reset Terminal	Crst		-	-	20	рF
Pulse Width of						
Asynchronous Reset	Trst		100	-	-	μsec
Power On Startup Time	Tstart		-	-	10	nsec

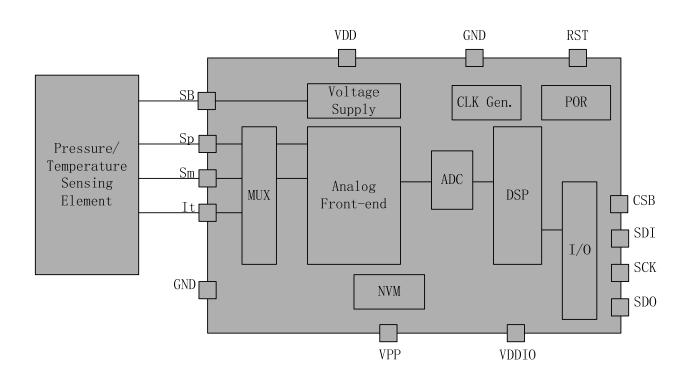
Oversampling setting	Pressure Oversampling	Temperature Oversampling		ODR @standby 1ms Typ	Average Current Typ @Isample/sec force-mode	rn <b>s</b> Noise Typ.
uni t	-	-	n <b>s</b> ec	Hz	μA	Pa
Hgh speed	2	1	5. 5	153	4.1	5. 2
Low power	4	1	7. 2	121	5. 2	3 7
Standard	8	1	10.6	86	7. 3	26
Hgh accuracy	16	2	18 3	51	12	1. 8
Utra Hgh accuracy	32	4	33.7	28	21. 4	1. 3

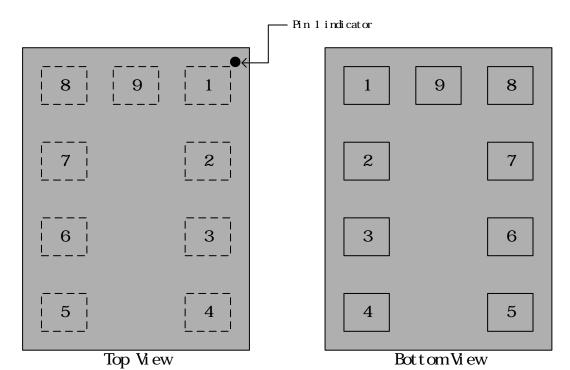
At Ta=25 degC, VDD=1.8V, CPU Clock Frequency=300kHz, unless otherwise noted

	Typical rms Noise in Pressure [Pa]							
Oversampling setting	IIR filter coefficient							
	off	2	4	8	16	32		
Hgh speed	5.2	25	1. 6	1. 1	0.8	0.5		
Low power	3 7	1. 8	1. 1	0.8	0.5	0.4		
Standard	26	1. 3	0.8	0.5	0.4	0.3		
Hgh accuracy	1. 8	0.9	0.6	0.4	0.3	0.3		
Utra Hgh accuracy	1. 3	0.6	0.4	0.3	0.3	0.2		

	Typical Bandwidth [Hz]							
Oversampling setting	IIR filter coefficient							
	off	2	4	8	16	32		
Hgh speed	133	30.7	12.8	5.9	29	1. 4		
Low power	108	24.9	10. 4	4.8	23	1. 1		
Standard	79	18 2	7. 6	3 5	1. 7	0.8		
Hgh accuracy	49	11. 3	4.7	22	1. 1	0.5		
Utra Hgh accuracy	28	6.5	27	1. 2	0.6	0.3		

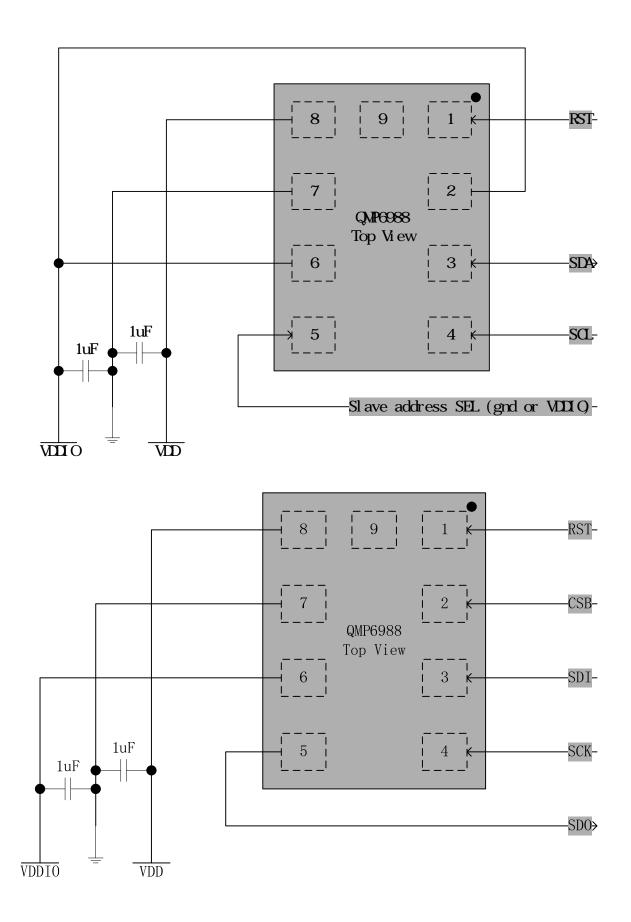
					Speci fi cati o	n (Typ.)	
		Pressure	Tempo		Current		
Example	Oversampling	oversampling	oversampling	IIR filter	consumption	ODR [Hz]	rms Noise
use case	Setting	times	times	coefficient	[uA]	(Example)	[Pa]
Weat her	II ale anno al	2	1	off	1. 2	0.05	5. 2
nomitoring	Hgh speed	4		0.0	407	100	0 7
Drop detection	Low power	4	l	off	407	100	3 7
Elevator detection	Standard	8	1	4	63.4	10	0.8
Stair detection	High accuracy	16	2	8	219	20	0.4
Indoor navigation	Utra high accuracy	32	4	32	570	28	0.2

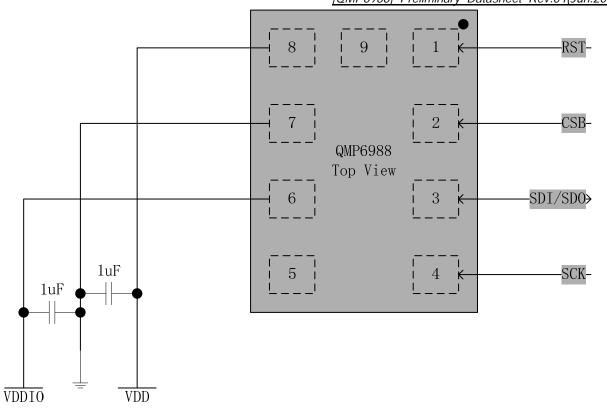




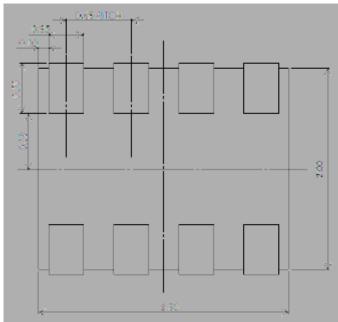
Pin Description

1111	DSCI PCI	011						
No.	Crashal	Descri	iption					
IVO.	Syntbol	SPI	I 2C					
1	RST	Asynchronous Reset *1)						
2	CSB	CSB	VDIO					
3	SDI	SDI/SDO	SDA					
4	SCK	SCK	SCL					
5	SDO	SDO	ADDR					
6	VDI O	Power Supply	Power Supply to Digital IO					
7	G/D	Ground						
8	VDI O	Power Supply						
9	VPP	Power Supply to N	WMPrograming *2)					









(Top View) : Recommended



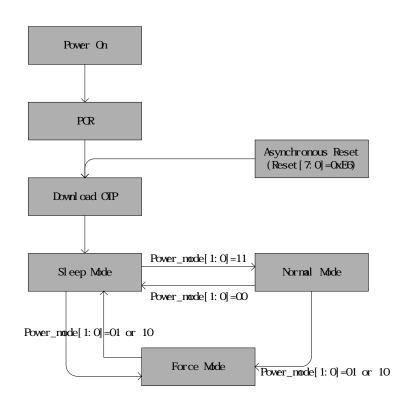
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Communication Mode	CSB	SDI	SCK	SDO	Renark
I 2C	WII O	SDA	SCL	0/1	SDO=0 70h, SDO=1 56h
SPI 3 wires	CSB	SII/SIX	SCK	-	spi3wregister = 1
SPI 4 wires	CSB	SDI	SCK	SDO	spi3wregister = 0

1) 2)

3)

4)



1)

2)

3)

$$Pr = b00 + bt1 \bullet Tr + bp1 \bullet Dp + b11 \bullet Tr \bullet Dp + bt2 \bullet Tr^{2} + bp2 \bullet Dp^{2}$$
$$+b12 \bullet Dp \bullet Tr^{2} + b21 \bullet Dp^{2} \bullet Tr + bp3 \bullet Dp^{3}$$

lacktriangle

K	Conversi (	on factor		OIP	
V	Α	S	23-16bit	15-8bit	7-Obit
a1	-6.30E-03	4.30E-04	-	CCE_a1_1	CCE_a1_0
a2	- 1. 90E- 11	1. 20E-10	-	COE_a2_2	CCE_a2_0
bt 1	1. OOE- O1	9. 10E-02	-	COE_bt 1_1	CCE_bt 1_0
bt2	1. 20E-08	1. 20E-06	-	COE_bt 2_1	CCE_bt 2_0
bp1	3.30E-02	1. 90E-02	-	COE_bp1_1	CCE_bp1_0
b11	2.10E-07	1. 40E-07	-	COE_b11_1	CCE_b11_0
bp2	- 6. 30E- 10	3.50E-10	-	COE_bp2_1	CCE_bp2_0
b12	2 90E-13	7. 60E-13	-	COE_bp12_1	CCE_bp12_0
b21	2 10E-15	1. 20E-14	-	COE_bp21_1	CCE_bp21_0
bp3	1. 30E-16	7. 90E-17	-	COE_bp3_1	CCE_bp3_0

V	Conversion factor	OIP							
V	Conversion ractor	19-12bit	11-4bit	3-Obit					
a0	Offset value (200,16)	COE_aO_1	COE_aO_0	CCE_aO_ex					
b00	Offset value (200,16)	COE_bOO_1	COE_bOO_0	CCE_b00_ex					

•

bi t	24	23	22	 5	4	3	2	1	Note
22bits output	D21	D2O	D19	 D2	D1	$\mathbf{m}$	0	0	Temp/Press_ave=001
23 bits output	D22	D21	D20	 $\mathbb{D}_{3}$	D2	D1	$\mathbf{m}$	0	Temp/Press_ave=010
24bits output	D23	D22	D21	 D4	$\mathbb{B}$	D2	D1	$\mathbb{D}$	Temp/Press_ave=011~111

$$Dt = ((TEMP \_ TXD2) << 16) + ((TEMP \_ TXD1) << 8) + (TEMP \_ TXD0) - pow(2,23)$$
  
 $Dp = ((PRESS \_ TXD2) << 16) + ((PRESS \_ TXD1) << 8) + (PRESS \_ TXD0) - pow(2,23)$ 

Register Name	I <sup>2</sup> C Addr.	SPI Addr.	Length	R/W	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0	initial

Bit7~5 t\_standby[2:0]: Standby time setting

000	001	010	011	100	101	110	111
1ms	5ms	50ms	250ms	500ms	1s	2s	4s

Bit3~4 Reserved: keep these bits at 0

Bit2 spi3\_sdim[2]: select output type of SDI terminal 0: Lo / Hiz output

0: Lo / Hiz output 1: Lo / Hi output

Bit1 Reserved: keep this bit at 0

Bit0 spi3w[0]: Change mode between SPI 4-wire and SPI 3-wire

0: 4-wire (default)

1: 3-wire

Register Name	I <sup>2</sup> C Addr.	SPI Addr.	Length	R/W	bit7	bit6	bi	t5	bit4	bit3	bit2	bit1	bit0	initial
Bit7~5 temp_	average[2:0	)] A	Average 1	times se	tting fo	or temp	erature	e mea	asureme	ent (skip	means	no mea	suremen	t)
•	•		000	001	010 0	)11	100	101	110	111				
			Skip	1 2	2 4	1 8	3	16	32	64				
Bit4~2 press_	average[2:0	)] A	Average	times se	tting fo	or press	ure m	easur	rement (	skip me	eans no	measure	ement)	
			000	001	010	)11	100	101	110	111				
			Skip	1 2	2 4	1 8	3	16	32	64				
Bit1,0 power	_mode[1:0]		Operation	n mode	setting									
			)Ô: s	sleep m	ode									
		(		force m										
		1	1: 1	normal	mode									

Register Name	I <sup>2</sup> C Addr.	SPI Addr.	Length	R/W	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0	initial

Bit7~4 Reserved: Keep these bits at 0

Bit3 measure Device operation status. This value automatically changes

0: finish a measurement – waiting for next measurement 1: on a measurement – waiting for finishing the data store

Bit2~1 Reserved: Keep these bits at 0

Bit0 otp\_update the status of OTP data access. This value automatically changes

0: no accessing OTP data1: while accessing OTP data

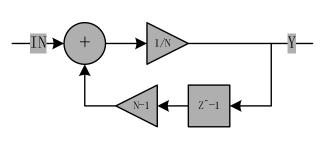
Register Name	I <sup>2</sup> C Addr.	SPI Addr.	Length	R/W	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0	initial
- V													

bit7~3 Reserved: Keep these bits at 0

bit2,1,0 master\_code[2:0] Master code setting at I2C high-speed mode.

000	001	010	011	100	101	110	111
0x08	0x09	0x0A	0x0B	0x0C	0x0D	0x0E	0x0F

	Register Name	I <sup>2</sup> C Addr.	SPI Addr.	Length	R/W	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0	initial
ſ														



$\infty$							
Œf	N <u>+</u> 2	N=4	N=8	N=16	N=32	N=32	N=32

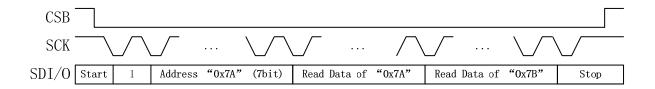
I	Register Name	I <sup>2</sup> C Addr.	SPI Addr.	Length	R/W	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0	initial
ſ														

Bit7~0 reset[7:0]

When input "E6h", the software reset will be effective. Except for that, nothing is to happen.

Register Name	I <sup>2</sup> C Addr.	SPI Addr.	Length	R/W	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0	initial
Bit7~0 chip_id	1[7:0]	5C											

SDO	I 2C Slave	bi+	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0
SLU	Address (7bits)	bi t	Add[6]	Add[5]	Add[4]	Add[3]	Add[2]	Add[1]	Add[0]	R/W
H gh(1)	56h + R/W	Value	1	0	1	0	1	1	0	1/0
Low(O)	70h + R/W	Value	1	1	1	0	0	0	0	1/0



.

HPDU

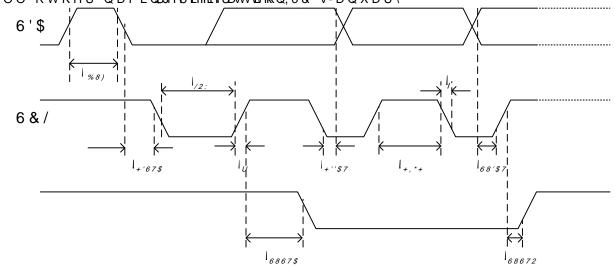
,QWHUIDFH VSHFLILFDWLRQV

,ð& WLPLQJV

\$00 WLPLQJV DSHSDOW, 6WRDQGNHEEUSGY ONFEGSHY DW)DVW OERSGMHDVDYQ+GLJK 06 SHH,G5 80 RGHWLPLQJV WKH IROORZLQJ DEEUHYLDWLRQV DUH XVHG

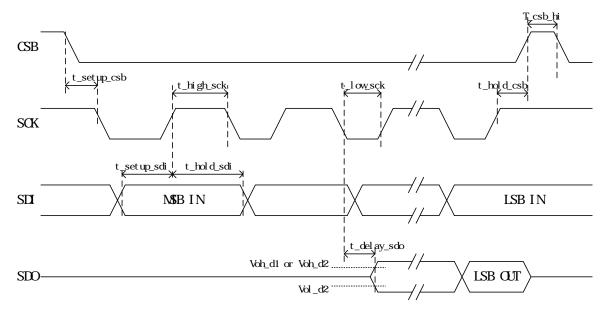
6 ) ORGH VWDQGDUG DQG IDVW PRGH

&É EXV FDSDFLWDQFH RQ 6', OLQH \$OO RWKHU QDPLCSJH BHIILHFLDWWRQ, ŏ& V-DQXDU\

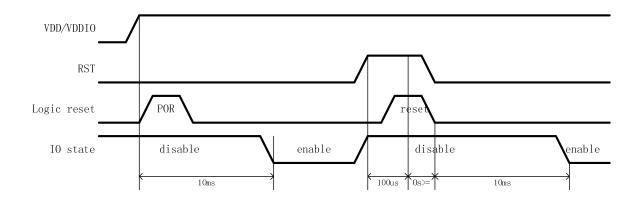


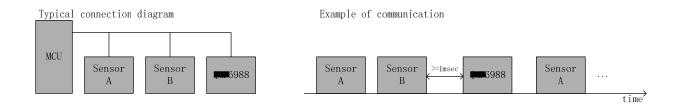
## 8QGHVFULEHG LWHPVFDROPG\$OVLDFQRWOZ/LDWUKHFWD1kWHLR,6Q& VSHFLI

, W H P V	6 \	PERO	&RQGL	WLRQ	PL	Q	$N \setminus S$	PD	8 Q L	WΥ	5
		6 ) 0 R G	Н					Q١	/		
6', 6HWXS	WLP	H+6W06R8G\$	₩	ΥL	R	9		Q٧	/		
		+6 0RG	Н	9 L F	1	9		Q٧	/		
		6 ) 0 R G	H & E	S)				Q٧	/		
		6 ) 0 R G	H & E	S)				Q٧	/		
				9 L R	9			Q١	/		
		+6 0RG	H & E	9 L 🕄 )	9			Q١	/		
				9 L R	9			Q١	/		
6', KROG	WWL₽'⊩	\$+76 ORG	H & E	9 L 🕄 )	9			Q١	/		
60 007 0	V O V I	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	16 O D	9LR GHD & E	9	(		Q \	ł T		
6&. ORZ S	\	T VV / 2.	+0 UK	9 [R & =	9	<u>)</u>		Q١	<b>/</b>		

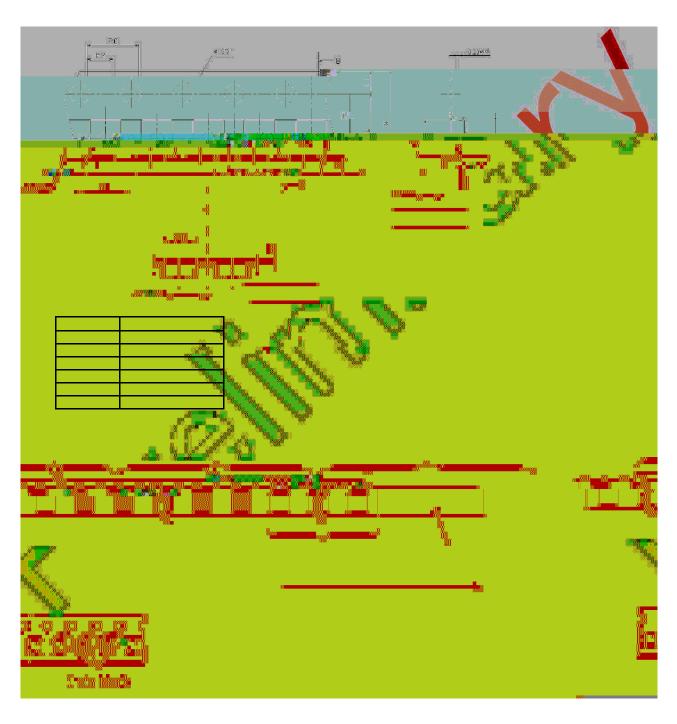


Iten <b>s</b>	Syntbol	Condition	min	typ	nax	Units	Renaurk
SCK frequency	f_spi		-	-	10	MHz	
SCK low pulse	t_l ow_sck		40	-	-	ns	
SCK high pulse	t_high_sck		40	-	-	ns	
SDI setupt time	t_setup_sdi		20	-	-	ns	
SDI hold time	t_hold_sdi		20	-	-	ns	
SDO out put delay	+ dol ov odo	Cb=25pF, Vio=1.62V min	-	-	30	ns	
SEO OUL PUL CIETAY	t_deray_sub	Cb=25pF, Vio=1.2V min	-	-	40	ns	
CSB setup time	t_setup_csb	-	40	-	-	ns	
CSB hold time	t_hold_csb		40	-	-	ns	
CSB_H time	t_csb_hi		100	-	-	ns	

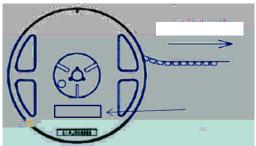


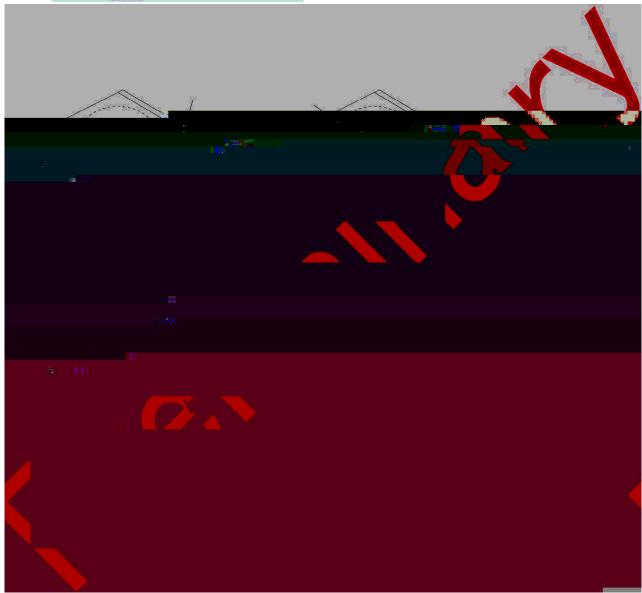


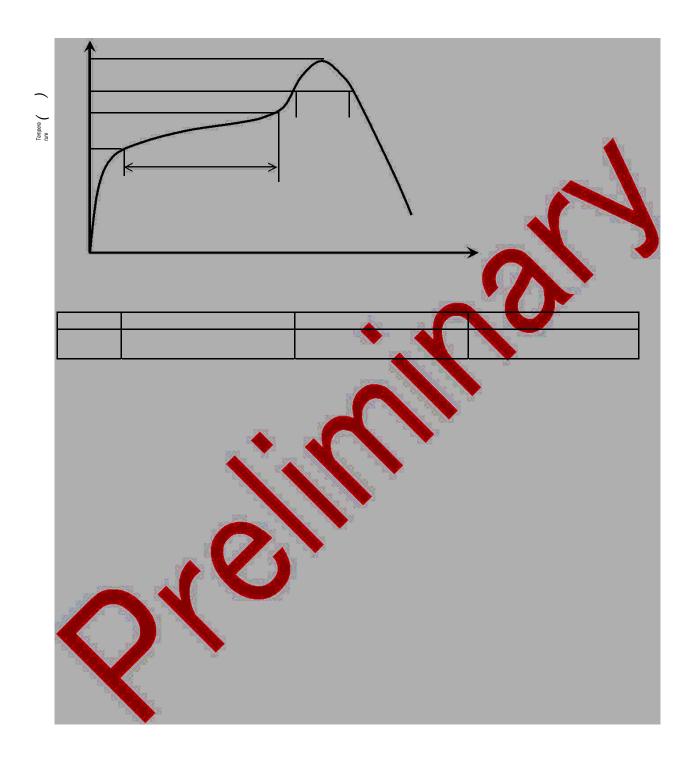
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(1) 1) 2)

3)

4)

5)

6)

(2) 1)

> 2) 3)

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10) 11)

12)

13)

14) 15)

16)

(3) 1) 2) 3) 4) 5) 6)